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PATENT, TRADEMARK
AND COPYRIGHT LAW

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Date: March 3, 2004

Attorney Docket No. NIT-416

CUSTOMER NO. 24956

To: Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir: Transmitted herewith for filing is the patent application of:

Inventor: N. TAKAURA et al (see attached)

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Enclosed are:

- ☒ 34 Sheets of Drawings
- ☒ This application is being filed without an executed Declaration.
- ☒ Priority is claimed from Japanese Application No. 2003-145305 (which is hereby
filed May 22, 2003 ☒ A certified copy is attached herewith. incorporated
Copies of the disclosure documents listed on the attached PTO 1449 form and by reference
☒ discussed in the specification or ☒ attached Information Disclosure Statement.
- ☐ Small entity status under 37 CFR 1.9 and 1.27 is hereby claimed.
- ☒ Specification: Abstract X, Description 49 pages; and 18 claim(s).
- ☐ Preliminary Amendment.
- ☐ Executed Declaration.

The filing fee is calculated as shown below:

For:	No. Filed	No. Extra
Basic Fee		
Total Claims	18 - 20 = *	0
Indep Claims	5 - 3 = *	2
<input type="checkbox"/> Multiple Dependent Claim (s)		

* If difference is less than zero
then enter '0' in second column

Small Entity

Rate	Fee
	\$ 375
x 9	\$
x 42	\$
+ 140	\$
Total	\$

OR

Large Entity

Rate	Fee
	\$ 770
x 18	\$ 0
x 84	\$ 172
+ 280	\$ 0
Total	\$ 942.

☒ A check or credit card payment form in the amount of \$ 942.00 is enclosed for the filing fee.

☒ The Commissioner is hereby authorized to charge any additional fees that may be required to
Deposit Account No. 50-1417.

Respectfully Submitted,

By:

John R. Mattingly

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